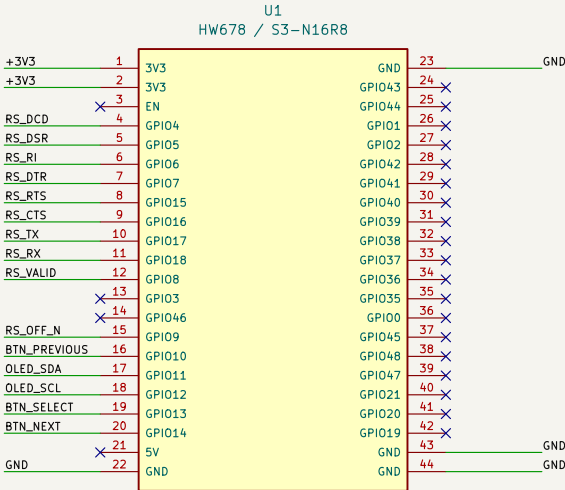


MODULE CARRIER – INITIAL SCHEMATIC

Native KiCad 10 schematic. Named wire labels join matching nets on this sheet.
Read hardware/PCB/README.md before layout or fabrication.

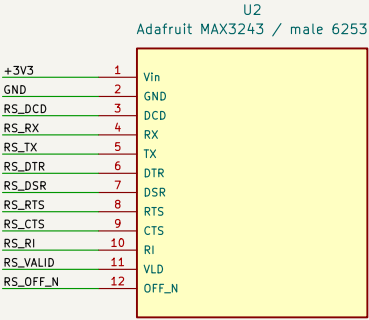
U1: complete development board

Antenna up / USB down: left row 1–22, right row 23–44.
NC marks mean unused on carrier, not unused inside module.

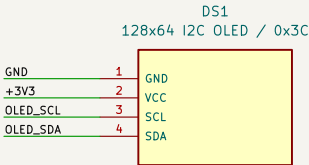


U2: complete RS–232 breakout

Male 6253 selected. Numbers from published 5988 female CAD.
VERIFY male header before routing; no footprint assigned.
DE–9 and charge pump are already on the module.



DS1: front view GND / VCC / SCL / SDA

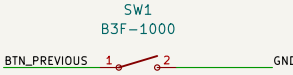


Power at 3.3 V only. Verify on–module I2C pull–ups;
external values intentionally not guessed.

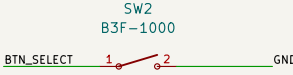
RS–232 / ELECTRICAL LIMITS
TX/RX and modem labels are at the ESP32 logic side.
OFF_N is active low; GPIO9 must remain open–drain.
Breakout provides its OFF pull–up. No galvanic isolation.
No carrier connection to raw RS–232 voltages.
Carrier GND, USB GND and RS–232 signal ground are common.
Never power OLED or MAX3243 logic from 5 V.

Controls: active–low, firmware–debounced

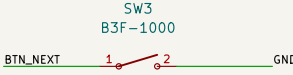
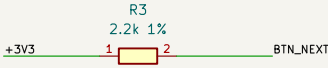
External 2.2k pull–ups: ~1.5 mA pressed at 3.3 V.
Chosen for B3F–1000 rated contact load; internal
pull–ups may remain enabled. Exact resistor MPN pending.



Previous / back



Select / confirm



Next

BEFORE PCB RELEASE
Verify male breakout pin order/footprint.
Confirm header/socket finished–hole requirements.
Complete module courtyards and antenna clearance.
Verify 3.3 V budget, OLED pull–ups and USB power path.
Choose mounting hardware and confirm button stock.
ERC is not hardware or fabrication approval.

Not fabrication–ready: provisional module footprints and male RS–232 mapping

Sheet: /
File: serial–carrier.kicad_sch

Title: ESP32 Serial Swiss Army Knife – Module Carrier

Size: A3 Date: 2026–09–20

Rev: A0 DRAFT

KiCad E.D.A. 10.0.6

Id: 1/1